Design a microstrip ring hybrid for a 50 Ω system with a design frequency of 2 GHz on Rogers RT/duroid 5880 (1 oz. copper, 1.574 mm board thickness). Draw a fully-labeled top view sketch of design.

Per section 7.8 and Figure 7.42a, we will need $Z_0 = 50 \Omega$ and $\sqrt{2} Z_0 = \sqrt{2} (50) = 70.71 \Omega$ microstrips.

The Rogers RT/duroid 5880 datasheet only gives $\varepsilon_r = 2.20$ (no graph versus frequency).

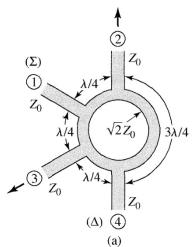
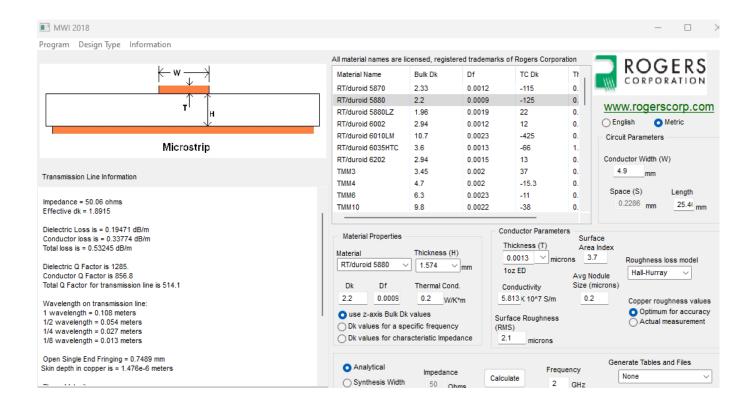


FIGURE 7.42 Three types of hybrid junctions. (a) A ring hybrid, or *rat-race*, in microstrip line or stripline form.

$Z_0 = 50 \Omega$

From Rogers MWI at 2 GHz on Rogers RT/duroid 5880 (1 oz. copper, 1.574 mm thick)- $\underline{W_{50}} = 4.9 \text{ mm}$. Started with 'Synthesis Width' option and changed to 'Analytical' option to get closer values for 50 Ω , only went to one decimal place for width.



$Z_0 = 70.71 \ \Omega$

From Rogers MWI at 2 GHz on Rogers RT/duroid 5880 (1 oz. copper, 1.574 mm thick)- $\underline{W_{71}} = 2.807 \text{ mm} \approx 2.8 \text{ mm}$ and $\varepsilon_{r,e} = 1.8299$. Started with $W_{50} = 4.9 \text{ mm}$ and used the 'Analytical' option to get width value by trial-n-error.

Use (3.193) to get $v_p = c / \sqrt{\varepsilon_{r,e}} = 2.9979 \times 10^8 / \sqrt{1.8299}$ $\Rightarrow v_{p,71} = 2.216 \times 10^8 \,\text{m/s}.$

 $\lambda_{71} = v_{p,71}/f = 2.216 \times 10^8/2 \times 10^9 = 110.8086 \text{ mm} \Rightarrow 3\lambda_{71}/4 = 83.1 \text{ mm} \& \lambda_{71}/4 = 27.7 \text{ mm}.$

