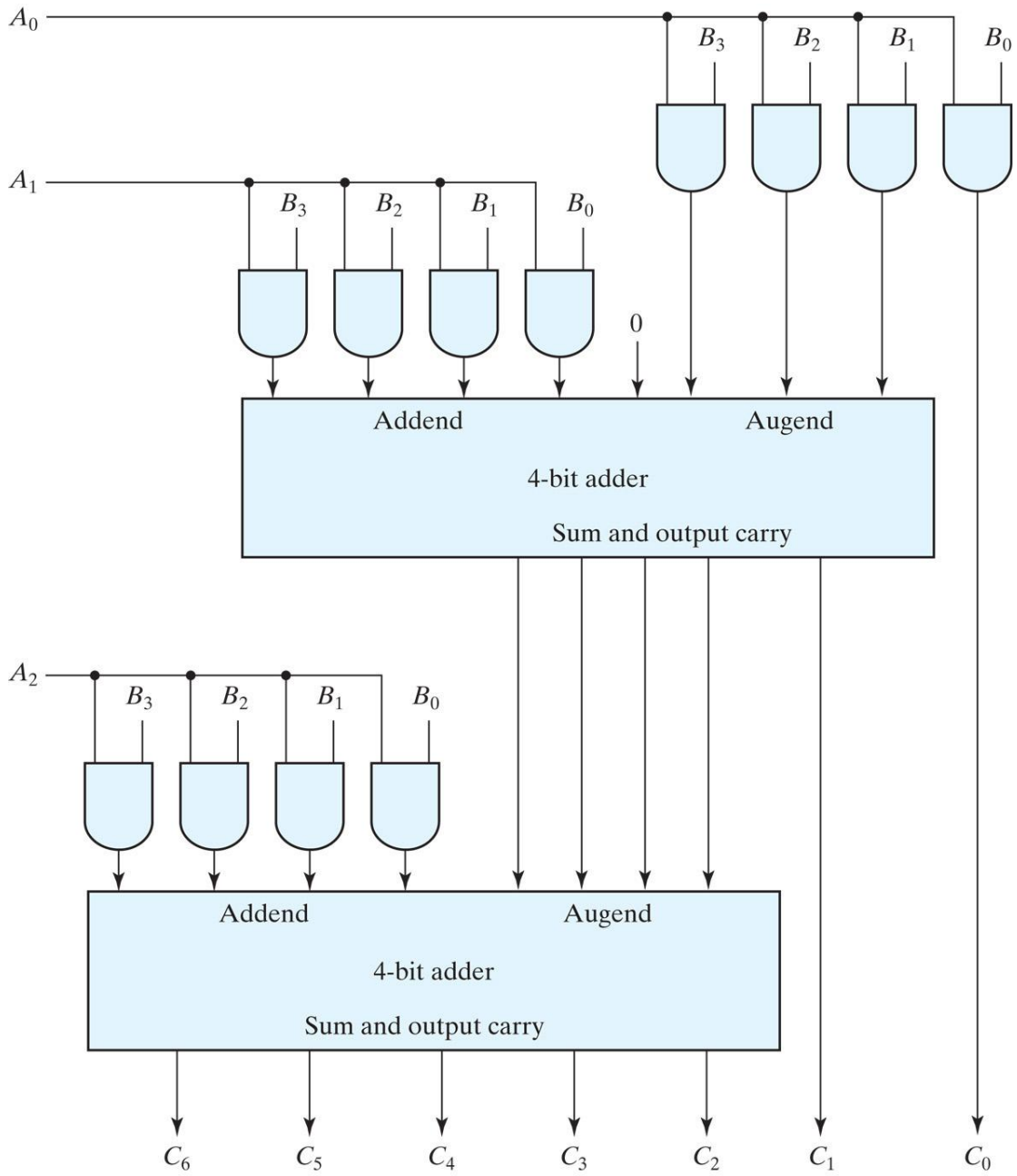


4-bit by 3-bit Multiplier (Figure 4.16)



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Note: The multiplier A has 3 bits ($A_2 A_1 A_0$) while the multiplicand B has 4 bits ($B_3 B_2 B_1 B_0$).